DMG48480C021_02WTC

Features:

- Based on T5L1, running DGUS II system, commercial grade.
- 2.1-inch circular LCD, 480*480 pixels resolution, 16.7M colors, IPS-TFT-LCD, wide viewing angle.
- Circular CTP.





1.Hardware and interface

1.1 Hardware interface



Hardware interface

1.2 Hardware and interface description

No.	Name	Description
1	T5L1 ASIC	Developed by DWIN. Mass production in 2019,1MBytes Nor Flash on the chip, 512KBytes used to store the user database. Rewrite cycle: over 100,000 times
2	LCM interface	FPC40_0.5mm, RGB interface
3	CTP interface	FPC6_0.5mm, IIC interface
4	User interface	10Pin_1.0mm latching socket for power supply and serial communication. Download rate(typical value): 12KByte/s
5	Flash	16MBytes NOR Flash, for fonts, pictures and audio files. Rewrite cycle: over 100,000 times
6	Buzzer	3V passive buzzer
7	SD card interface	FAT32. Download files by SD card interface can be displayed in statistics. Download rate: 4Mb/s
8	PGT05 interface	When product crashes by accident, you can use PGT05 to update DGUS kernel and make the product return to normal

2.Specification parameters

2.1 Display parameters

LCD Type	IPS, TFT LCD				
Viewing Angle	Wide viewing angle,85°/85°/85°(L/R/U/D)				
Resolution	480×480 pixels (support 0°/90°/180°/270°)				
Color	24-bit 8R8G8B				
Active Area (A.A.)	D=53.3mm				
Backlight Mode	LED				
Backlight Service Life	>20000 hours (Time of the brightness decaying to 50% on the condition of continuous working with the maximum brightness)				
Brightness	350nit				
Brightness Control	0~100 grade (When the brightness is adjusted to 1%~30% of the maximum brightness, flickering may occur and is not recommended to use in this range)				
Note:You can use dynamic screen saver wallpapers to avoid afterimages caused by fixed page display for a long time.					

2.2 Touch parameters

Туре	CTP (Capacitive touch panel)
Structure	G+F structure with surface cover of tempered glass
Touch Mode	Single touch, support continuous sliding touch
Surface Hardness	6Н
Light Transmittance	Over 90%
Life	Over 1,000,000 times touch

2.3 Serial interface parameters

Mode	UART2: ON=TTL/CMOS; OFF=RS232 UART4: ON=TTL/CMOS; OFF=RS232 (Only available after OS configuration)					
	Test Condition	Min	Тур	Max	Unit	
	Output 1, lout = -4mA	2.7	3.2	-	V	
Voltage Level	Output 0, lout = 4mA	-	0.1	0.4	V	
	Input 1	2.4	3.3	5.5	V	
	Input 0	0	-	1.0	v	
Baud Rate	3150~3225600bps, typical value of 115200bps					
Data Format	UART2: N81 UART4: N81/E81/O81/N82 , 4 modes (OS configuration)					
Interface Cable	10Pin_1.0mm					

2.4 Electrical specifications

Rated Power	<5W			
Operating Voltage	4.5~5.5V, typical value of 5V			
	140mA	VCC=5V, max backlight		
Operating Current	70mA	VCC=5V, backlight off		
Recommended power supply: 5V 1A DC				

2.5 Operating environment

Operating Temperature	-20℃~70℃ (5V @ 60% RH)
Storage Temperature	-30℃~80℃
Conformal coating	None
Operating Humidity	10%~90%RH, typical value of 60% RH

3. Reliability test

3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: the product was placed on the test bench to perform contact and air discharge in turn of the serial screen iron frame and display area. During the experimental process, it was observed whether the screen is dead, black, white, splash, or reboot. According to the experiment results, the performance is in line with the criteria GB/T 17626.2 B level and above.

Discharge Type	Discharge Value	Result		
Air discharge	±4KV	Normal operation		

4. Packaging & dimensions

Form Factor	71.3 (W) mm×71.3 (H) mm×11.7 (T) mm 60g						
Net Weight							
Packaging Standar	ds						
Model	Dimensions	Layer	Quantity/Layer	Quantity(Pcs)			
Carton1:	220mm(L)×160mm(W)×47mm (H)	-	-				
Carton2:	250mm(L)×200mm(W)×80mm (H)	2	2	4			
Carton3:	320mm(L)×270mm(W)×80mm (H)	2	4	8			
Carton4:	450mm(L)×350mm(W)×300mm(H)	-		-			
Carton5:	600mm(L)×450mm(W)×300mm(H)	2	60	120			

Disclaimer: The product design is subject to alternation and improvement without prior notice.

ideal	partner j	or you	Pro	fessi	ional	, Cre	editable, Successful Proc	luct Specification
+2V	RX4	RX2	TX2	TX4	GND	Definition	1053 3 100 11 1 2 3	
1, 2, 3	4	5	6	7	8, 9, 10	Pin#		
p	Ι	I	0	0	Р	Туре		
Power Input	UART4 Input	UART2 Input	UART2 Output	UART4 Output	GND	Description	Black	
Thit	Scale	Drawing	Mode1				59. 7 (LCD OD)	
MM		A 4						
Annrova	Review	Drawn	DMG4					
		DWIN	DMG48480C021-02WTC	K	Note: /	2. Unmai		
Date	Date	Date	02WTC		Active a	cked Tol	ation hol	
1					rea is m	erance i	e is use	
	3	DV			arked ir	2. Unmarked Tolerance is +/-0.3mm	d as pos	
		DWIN Technology			Note: Active area is marked in Dash lines	3mm	000 UV glue fixed position, glue width Imm 1. Location hole is used as position reference.	
		chnolo			nes		glue wid	
		gy					ch Imm	

8

5. Debugging tools

It is recommended for new users of DWIN smart LCMs to purchase official accessories. For more details, please refer to customer service center.



6.T5L series IC features

(1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L

is up to 250MHz, 1T(single instruction cycle)high speed operation.

- (2) Separate GUI CPU Core running DGUS II System:
 - High-speed display memory, 2.4GB/S bandwidth.
 - 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
 - Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
 - Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
 - 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
 - 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
 - Support DGUS development and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in

practical application:

- Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
- 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
- 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
- Support IAP on-line simulation and debugging with unlimited number of breakpoints.
- Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40 $^{\circ}$ C to +85 $^{\circ}$ C (IC operating temperature customizable from -55 $^{\circ}$ C to 105 $^{\circ}$ C).

DWIN encourages users to design your own customized product based on T5L

7.Revision records

Rev	Revise Date	Content	Editor		
00	2023-07-06	First Edition	Xu Ying		

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products:

- Customer service Tel: +86 400 018 9008
- Customer service email: dwinhmi@dwin.com.cn
- DWIN Developer Forum: <u>https://forums.dwin-global.com/</u>

Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!